



WHAT DOLPHIN CUSTOMERS ARE SAYING

“THE BEST RAM TECHNOLOGY”

As VP of IC Engineering at Tiler Corporation and a customer of TSMC, I am submitting this testimonial for Dolphin Technology, an IP provider for TSMC. Tiler Corporation has used Dolphin Technology RAMs, ROMs, and I/Os across the 90nm, 40nm and 28nm nodes.

Tiler benchmarks consistently show that Dolphin has the best RAM technology, beating other top vendors in all dimensions – frequency, area and power. Dolphin’s I/O IP incorporated important features starting in 90nm and below, technologies that other leading vendors have yet to implement in 28nm.

Dolphin understands that Tiler is a small company and provides flexibility and early access to technology. They support Tiler during design and well past first silicon, helping debug our issues, accepting feedback, and continuing to tune and improve their offerings. For

example, Dolphin kept Tiler informed and updated as they tuned IP to accommodate the 6σ process variations seen in recent technologies. Dolphin fully embraces making their customers successful.

Dolphin has continued to expand their IP offerings to include extensive standard cell libraries, DfT tools and protocol controllers. The growth is planned and pertinent. Recently, they have instituted a customer tracking system that is increasing the quality of release mechanisms and product collateral.

Dolphin Technology has been an excellent partner for 8+ years.

*John F. Brown III
Vice President, IC Engineering
Tiler Corporation*

“A TRUSTED SUPPLIER AND PARTNER”

Since 2007, Sigma Designs has utilized Dolphin Technology’s products in multiple projects across multiple process nodes. This includes Dolphin IP such as SRAM Memory Compilers (Single Port, Dual Port, 1-Port and 2-Port), Via ROM Memory Instances, Standard Cell Libraries (both High Density and High Speed) and DDR PHY Interfaces.

The first process node where Sigma used Dolphin IP was 90GP, and we have since used it in 55GP and 40LP as

well. Dolphin has repeatedly met our highest expectations about the quality of both the IP and the ongoing support we receive, and they have firmly established themselves as a trusted supplier and partner.

*Jacques Martinella
Vice President, Engineering
Sigma Designs*

“THE BEST DENSITY AND SPEED AT MINIMUM POWER”

We at GEO have used Dolphin Technology’s IP in several products, and each time, we have been very pleased with the results.

In particular, we have found Dolphin’s SRAM Compilers to provide the best density and speed at minimum power.

Their Standard Cell and I/O Libraries are also highly competitive in density, features, power and speed.

*Sudhir Chandratreya
Vice President, Engineering
GEO Semiconductor*

Front End views are available under NDA. For more information, [click here](#) or contact sales@dolphin-ic.com